

## Refine Search

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### Search Results -

Terms	Documents
L33 or L32 or L31 or L30 or L29 or L28 or L25	25

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**Database:** US Pre-Grant Publication Full-Text Database  
 US Patents Full-Text Database  
 US OCR Full-Text Database  
 EPO Abstracts Database  
 JPO Abstracts Database  
 Derwent World Patents Index  
 IBM Technical Disclosure Bulletins

**Search:**

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### Search History

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**DATE:** Wednesday, March 17, 2004 [Printable Copy](#) [Create Case](#)

**Set Name Query**

side by side

**Hit Count Set Name**  
result set

<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
<u>L34</u>	133 or l32 or l31 or l30 or l29 or l28 or l25	25 <u>L34</u>
<u>L33</u>	L26 and l21	4 <u>L33</u>
<u>L32</u>	L27 and l21	2 <u>L32</u>
<u>L31</u>	L27 and l3	7 <u>L31</u>
<u>L30</u>	L26 and l3	2 <u>L30</u>
<u>L29</u>	L26 and l24	9 <u>L29</u>
<u>L28</u>	L27 and l24	2 <u>L28</u>
<u>L27</u>	709/\$.ccls.	27167 <u>L27</u>
<u>L26</u>	714/\$.ccls.	44077 <u>L26</u>
<u>L25</u>	L24 and l1	1 <u>L25</u>
<u>L24</u>	l2 same (non-maskable interrupt\$1)	17 <u>L24</u>
<u>L23</u>	L21 and l13	1 <u>L23</u>
<u>L22</u>	L21 and l17	0 <u>L22</u>
<u>L21</u>	L19 and l7	72 <u>L21</u>

<u>L20</u>	L19 and l3	4	<u>L20</u>
<u>L19</u>	flush\$5 same cache\$1	3959	<u>L19</u>
<u>L18</u>	L17 same (operating system\$1)	3	<u>L18</u>
<u>L17</u>	insert\$5 near5 (break point\$1)	177	<u>L17</u>
<u>L16</u>	L14 and (break point\$1)	4	<u>L16</u>
<u>L15</u>	L14 same (break point\$1)	0	<u>L15</u>
<u>L14</u>	(new version\$1) near3 (operating system\$1)	152	<u>L14</u>
<u>L13</u>	switch\$5 near3 (address map\$5)	76	<u>L13</u>
<u>L12</u>	copy\$5 near5 (operating system\$1)	1462	<u>L12</u>
<u>L11</u>	L3 and l4	14	<u>L11</u>
<u>L10</u>	L3 and l5	1	<u>L10</u>
<u>L9</u>	L4 and l5	223	<u>L9</u>
<u>L8</u>	L7 and l5	3	<u>L8</u>
<u>L7</u>	(operating system\$1) same L6	410	<u>L7</u>
<u>L6</u>	fetch\$5 near3 instruction\$1	17052	<u>L6</u>
<u>L5</u>	breakpoint\$1 same L4	223	<u>L5</u>
<u>L4</u>	(stop\$5 or halt\$5) near5 (processor\$1 or computer\$1)	19530	<u>L4</u>
<u>L3</u>	l1 same L2	53	<u>L3</u>
<u>L2</u>	multiprocessor\$1 or multi-processor\$1 or multicompiler\$1	30885	<u>L2</u>
<u>L1</u>	event\$1 near3 handl\$5	9770	<u>L1</u>

END OF SEARCH HISTORY